

Compositional Testing of Communication Systems

Reinhard Gotzhein¹, Ferhat Khendek²

Abstract. In this paper, we propose the *compositional test method (C-method)*, which exploits the structure of component-based communication systems. The C-method first tests each component separately for output and/or transfer faults, using one of the traditional test methods, then checks for composability, and finally tests the composite system for composition faults. To check for composability and to derive the test suite for the detection of composition faults, it is not required to construct the global state machine. Instead, all information is derived from the component state machines, which avoids a potential state explosion and lengthy test cases. Furthermore, the test suite checks for composition faults only. This substantially reduces the size of the test suite and thus the overall test effort.

1 Introduction

Systematic methods for testing protocol implementations have a long and successful record. The relevance and the potential of protocol testing are first recognized in [16], which has initiated a research stream that has produced a diversity of test methods with different foci. These methods usually assume that the design of the protocol implementation to be tested is given in the form of a finite state machine (FSM), and that this state machine is minimal, completely specified, and fully connected. Some methods further assume the FSM to be deterministic [3,7,20], while others relax this constraint [12]. Recently, the focus has shifted to real-time systems testing [6,17,18], interoperability testing [1,2,4,5] and testing in context [14,15].

On the other hand, component-based software engineering is becoming an important trend among practitioners. This approach aims at shortening the development process and therefore reducing the cost. Once developed and tested, components are reused and glued together in different contexts. The testing of such systems formed by reused components remains an open and challenging issue [21], mainly because components are developed and reused by different people without or with very little information sharing.

The purpose of this paper is to propose a formal approach for testing component-based communicating systems, which we call *compositional testing (C-method)*. Here, communication systems are perceived as being built from components that can be mod-

1. Computer Science Department, University of Kaiserslautern, Postfach 3049, D-67653 Kaiserslautern, Germany, gotzhein@informatik.uni-kl.de

2. ECE Department, Concordia University, 1455 de Maisonneuve Blvd. W., Montréal, Québec, Canada H3G 1M8, khendek@ece.concordia.ca

eled as FSMs. Each of these components is tested using well-proven techniques, such as the UIOv-method [20] or the Wp-method [7]. However, when these components are composed, no monolithic FSM is constructed in order to derive test cases for the composite system, which would lead to lengthy test cases, large test suites, and a repetition of tests already performed on component level. Instead, the composite system is only tested for *composition faults*, i.e., faulty composition code (also called *glue code*) - a new type of fault that extends and complements the classical fault model. We position our compositional testing approach among the existing and related techniques that also view systems as a set of interacting components, such as interoperability testing, testing in context and other compositional testing techniques.

In this paper, we will develop these ideas up to a certain point, and illustrate them through examples. We focus on a specific type of composition, called *concurrent composition*. However, other types of composition may be considered as well. Section 2 defines the concurrent composition of asynchronously communicating FSMs, and states necessary conditions for composability. The *compositional test method (C-method)* is defined in Section 3. An application of the C-method is shown in Section 4. In Section 5, related work is reviewed and the contributions of this paper are positioned. We draw conclusions and indicate future research topics in Section 6.

2 Concurrent composition

In this section, we define the concurrent composition of two FSMs. Further types of composition such as sequential composition are perceivable, for instance, in the context of micro protocols [8] or general component-based software systems. At specification level, composition can be expressed by defining a *composition operator*. At implementation level, this operator is usually realized by a piece of code that we call *glue code*.

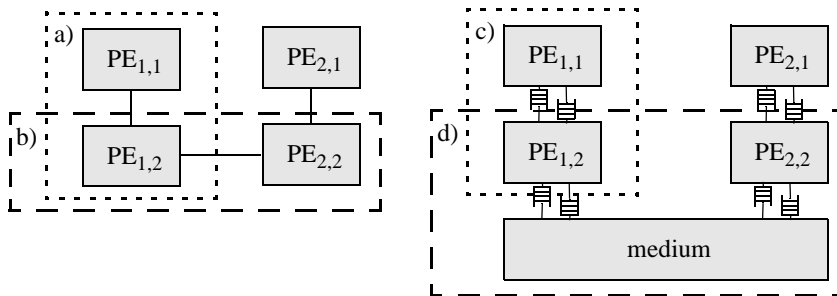


Figure 1 Concurrent composition of protocol entities

Concurrent composition may be applied to put local and/or remote components together. From the conceptual viewpoint, this should not make any difference. For instance, we may compose protocol entities PE_{1,1} and PE_{1,2} as well as PE_{1,2} and PE_{2,2} concurrently, as shown in Figure 1a and b, respectively. For the local composition, the glue code may consist of internal data structures and operations to add signals to the input queue of the other protocol entity (Figure 1c). For the remote composition, the glue code may comprise an entire logical communication medium, which may in turn be a

composite system (Figure 1d). From the practical viewpoint, the usual constraints concerning observability and controllability apply, which may be handled by external coordination procedures.

In this paper, we use the standard definition of FSM, and a derived notion:

Definition 1: A *finite state machine (FSM)* M is a tuple $(S, I, O, s_0, \lambda_e)$ with:

- S is a finite set of states.
- I is a finite input alphabet.
- O is a finite output alphabet.
- $s_0 \in S$ is the initial state.
- $\lambda_e \subseteq S \times I \times O \times S$ defines the *transitions* of M .

A finite state machine is completely specified, if for each state and each input, a transition is defined. There exist several ways to extend a given FSM to a completely specified machine, e.g., by assuming implicit transitions (cf. SDL [10]). The standard definition of FSMs (see Definition 1) does not distinguish between explicit and implicit transitions. We consider explicit transitions as regular behavior. Implicit transitions are undesired behavior, but included to enhance testability of the implementation. In this paper, we adopt this interpretation, but the proposed test method does work for any interpretation of implicit transitions.

Definition 2: A *completely specified finite state machine (csFSM)* $N = (S, I, O_e, s_0, \lambda)$ is derived from an FSM $M = (S, I, O, s_0, \lambda_e)$ as follows:

- S, I, s_0 as in M .
- $O_e = O \cup \{e\}$, where $e \notin O$ is called *error output*.
- $\lambda = \lambda_e \cup \lambda_i$ is the transition relation of N . Tuples of λ are called *transitions* of N .
- λ_e defines the *explicit transitions* of N .
- $\lambda_i = \{ (s, i, e, s') \mid s \in S \wedge i \in I \wedge \neg \exists o \in O, s' \in S: (s, i, o, s') \in \lambda_e \}$ defines the *implicit transitions* of N .

In the rest of the paper, we omit the error output e and the relation λ_i for brevity.

To define the concurrent composition of csFSMs, we assume that they communicate by asynchronous reliable signal exchange, where sending and receiving of signals is modeled as output and input of the communicating csFSMs, respectively. Therefore, an input queue collecting signals that are delivered, but not yet consumed, is associated with each csFSM. Furthermore, each signal carries identifications of the sending and receiving machine, which may be evaluated as needed. The identifications are determined dynamically from the sending machine, the connection structure of the communicating csFSMs consisting of typed channels, and explicit addressing, if necessary.

Definition 3: Let $N_1 = (S_1, I_1, O_1, s_{0,1}, \lambda_1)$ and $N_2 = (S_2, I_2, O_2, s_{0,2}, \lambda_2)$ be csFSMs. Let $O_{I_{1,2}} = O_1 \cap I_2$ ($O_{I_{2,1}} = O_2 \cap I_1$) be the set of signals exchanged between N_1 and N_2 (N_2 and N_1), called *internal signals*. The *concurrent composition* of N_1 and N_2 , denoted $N_1 \parallel N_2$, is defined by the derived state machine $Q = (S, I, O, s_0, \lambda)$ with:

- $S = S_1 \times I_1^* \times S_2 \times I_2^*$ is the set of states.
- $I = (I_1 - O_{I_{2,1}}) \cup (I_2 - O_{I_{1,2}})$ is the (finite) input alphabet.
- $O = (O_1 - O_{I_{1,2}}) \cup (O_2 - O_{I_{2,1}})$ is the (finite) output alphabet.

- $s_0 = (s_{0,1}, \langle \rangle, s_{0,2}, \langle \rangle)$ is the initial state, consisting of the initial states of N_1 and N_2 and the initial states of *input queues* associated with N_1 and N_2 , respectively.
- $\lambda \subseteq S \times I \times O \times S$ is the transition relation of Q . Tuples of λ are called *transitions* of Q . λ is derived from λ_1 and λ_2 as follows:

$$(s, i, o, s') \in \lambda \text{ with } s = (s_1, q_1, s_2, q_2) \text{ and } s' = (s_1', q_1', s_2', q_2') \text{ iff}$$

$$(\exists (s_1, i, o, s_1') \in \lambda_1: (q_1 = \langle i \rangle \wedge q_1' \wedge q_2' = \text{if } o \in OI_{1,2} \text{ then } q_2 \langle o \rangle$$

$$\text{else } q_2 \wedge s_2 = s_2')) \vee$$

$$(\exists (s_2, i, o, s_2') \in \lambda_2: (q_2 = \langle i \rangle \wedge q_2' \wedge q_1' = \text{if } o \in OI_{2,1} \text{ then } q_1 \langle o \rangle$$

$$\text{else } q_1 \wedge s_1 = s_1'))$$

This definition includes the concurrent composition of two independent csFSMs, i.e., two csFSMs that do not exchange signals. In this case, $OI_{1,2} = OI_{2,1} = \{\}$.

A csFSM can be represented as a labeled directed graph, where states correspond to nodes, and transitions correspond to edges labeled with input and output.

Definition 4: A *labeled directed graph* G is a tuple (V, L, E) , consisting of a set of nodes V , a set of labels L , and a relation $E \subseteq V \times V \times L$, defining the directed edges of the graph. A *path* is a non-empty sequence of consecutive edges. A *tour* is a path that starts and ends at the same node. It is called *minimal*, if no edge is contained more than once in the tour. An *initial tour* is a tour that starts and ends at the initial node. A directed graph G is *strongly connected*, if for each pair of nodes (v, v') , where $v \neq v'$, there is a path from v to v' .

Example 1: Figure 2 shows the concurrent composition of deterministic, strongly connected csFSMs N_1 and N_2 . Note that the error output as well as the implicit transitions are not shown in the figure. The machines interact via channel ch , which is typed by $OI_{1,2}$ and $OI_{2,1}$, and are connected to the environment by typed channels ch_1 and ch_2 . The resulting behavior after composition (see Figure 3) can be represented by the state machine $Q = N_1 \parallel N_2$, where states are represented as tuples (s_1, q_1, s_2, q_2) denoting the states of N_1 and N_2 , and of their input queues.

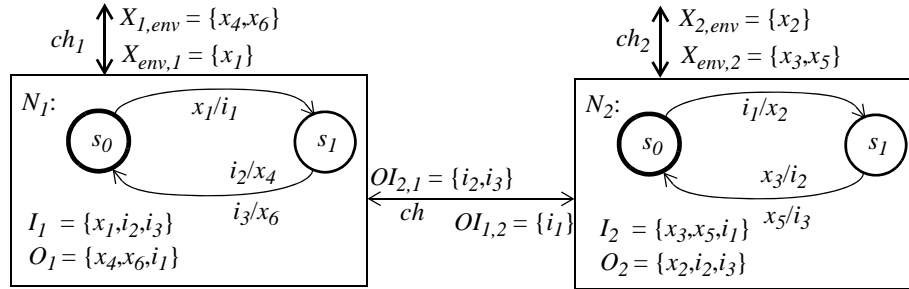


Figure 2 Concurrent composition: component machines N_1 and N_2 (Example 1)

While it is syntactically possible to compose all kinds of csFSMs, this is not always meaningful. Which csFSMs to compose first of all depends on the intended global behavior, which is problem specific. However, some *general composition criteria* can be stated:

- CC₁. Internal signals of either machine are eventually consumed by the other machine in an *explicit* transition, i.e., the composed system is *free of internal unspecified receptions*. This excludes transitions that have been added to obtain a completely specified state machine, i.e., implicit transitions yielding an error output (see Definition 2).
- CC₂. The composed system is free of *internal deadlocks*. Since it is assumed that external signals can be produced in any order, this again restricts the internal interaction only.

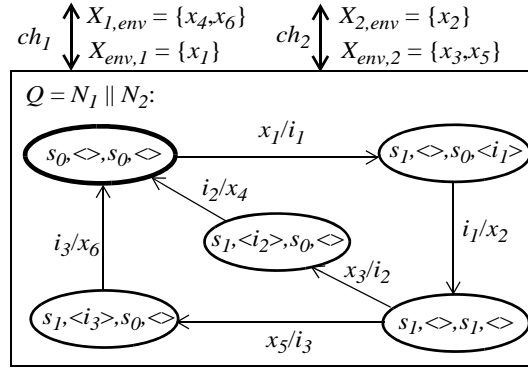


Figure 3 Concurrent composition: derived machine Q (Example 1)

3 Compositional testing of concurrently composed csFSMs

In this section, we will show how to derive test suites for testing the implementation of concurrently composed csFSMs. We make certain assumptions about the component csFSMs (e.g., strongly connected, deterministic) and their implementations (e.g., concerning the number of states), and we assume that the implementation of each csFSM can be tested using a test method that detects all output and transfer faults.

A direct approach to test the composite system would be to determine its global state machine, and then apply one of the existing test methods to derive test cases for this machine. This, however, has the following drawbacks:

- The state set of the global state machine may be very large. Firstly, this can consume considerable computational resources to determine the machine. Secondly, it can lead to a large test suite containing lengthy test cases, implying a testing effort that could quickly become unmanageable.
- The global state machine may be non-deterministic, due to the concurrency of the composite system, which reduces the applicability of existing test methods.
- All tests already executed at the component level are repeated. This is a severe drawback in general, and especially if components are to be reused in different protocol configurations.

To avoid these disadvantages, a test method satisfying the following properties is sought:

- It is not necessary to compute the global state machine.
- Only tests checking the correctness of the glue code of the csFSMs are derived.
- Tests already performed at the component level are not repeated.

These properties can only be satisfied if the implementations of the design components, which have been tested at the component level, remain unchanged. This means that only glue code to realize the specific type of composition is added, and all what remains to be checked in this case is the correct implementation of the composition operator.

In the following, we introduce a method for compositional testing - henceforth called *compositional test method (C-method)* - that satisfies the above properties. We start by defining the fault model, then introduce concepts, notations, and an initial tour coverage graph, and finally give a procedural definition of the C-method.

3.1 Fault model

The common way to check that a conformance relation that is defined on an infinite set of input sequences holds between two FSMs is to reduce the set of possible implementations to a finite number by assuming a fault model [13]. The classical fault model for protocol testing assumes that the implementation I can be treated as a *mutant* of the specification S , where a mutant may be obtained by altering outputs of transitions (*output faults*), by altering tail states of transitions (*transfer faults*), by adding states up to a given number as well as extra transitions to and from these states. This general fault model is sometimes reduced to output and transfer faults by assuming that the number of implementation states is less than a given maximum number, and to deterministic implementations.

Implementations are tested by applying input sequences and observing the output sequences. An implementation fault is detected, if an observed output sequence differs from the expected output sequence. Whether this fault is an output fault or a transfer fault, or due to an extra state or an extra transition, depends on the fault model, on the diagnosis capability of the test method, and on the knowledge about the implementation at the time of test execution.

The classical fault model is usually applied to single components that are specified by an FSM, e.g., a single protocol entity. It may also be applied to a composite system, e.g., protocol entities and an underlying medium, if an FSM of that system can be constructed. This, however, causes the aforementioned problems (large state spaces, non-determinism, repetition of tests). In order to avoid these problems, we propose to take the structural aspect of the composition into account, and to distinguish the following fault categories:

- *component fault*: the implementation of a *component* does not satisfy its specification
- *composition fault*: the *glue code* does not satisfy its specification in the given context

The problem of compositional testing can then be stated as follows:

Let N_1 and N_2 be the specifications of two components, and I_1 and I_2 be their implementations, where I_1 and I_2 satisfy their specifications N_1 and N_2 , respectively. Then, derive a minimal test suite that is sufficient to check whether the system I consisting of I_1 , I_2 , and glue code satisfies the specification $N_1 \parallel N_2$.

As usual, implementations are tested by applying input sequences, and comparing the observed and the expected output sequences. Again, it depends on the fault model, the diagnosis capability of the test method, and the knowledge about the implementation at the time of test execution how a detected fault may be classified. For instance, if the components have already been tested successfully, and their implementations are re-used in the composite system, then detected faults can be classified as composition faults.

To derive a minimal test suite that is sufficient to check the composed system, a model of the glue code is needed. In general, the glue code could be a component or a composite system itself, for instance, a logical communication medium, which may have further attached components. As testing would be unfeasible in this general setting, we make the following assumption:

- i) Whenever I_1 and I_2 are both in their initial states, the glue code is in a determined state w.r.t. I_1 and I_2 .
- ii) The behavior of the glue code is deterministic w.r.t. I_1 and I_2 .
- iii) If the glue code interacts with other components, this has no effect on its behavior towards I_1 and I_2 .
- iv) The glue code is not creating messages for I_1 or I_2 .

The first assumption limits the maximum length of test suites to the set of all initial tours, i.e., paths that start and end in the initial state. All assumptions together ensure that a finite number of test cases are sufficient.

Notice that if a model of the glue is given as an FSM, then the composition fault could be refined further into the same basic faults of an FSM based implementation.

3.2 Concepts and notations

The following definitions recall and introduce some concepts and notations for testing:

Definition 5: A *test case* tc is a non-empty sequence of inputs $i_1.i_2.\dots.i_n$. A *test suite* ts is a non-empty set of test cases $\{tc_1, tc_2, \dots, tc_m\}$. An *augmented test case* atc is defined as a non-empty sequence of transitions (also called *test elements*) $i_1/o_1.i_2/o_2.\dots.i_n/o_n$. An *augmented test suite* ats is a non-empty set of augmented test cases $\{atc_1, atc_2, \dots, atc_m\}$.

Definition 6: Let atc_1 and atc_2 be augmented test cases (sequences of transitions) of deterministic csFSMs N_1 and N_2 that communicate via a common channel ch with sets $O_{I_1,2}$ and $O_{I_2,1}$ of internal signals. The *concurrent composition* of atc_1 and atc_2 , denoted $atc_1 \parallel atc_2$, is one path $atc_{1,2}$ of the tree obtained by sequencing the test elements in atc_1 and atc_2 according to the following ordering constraints:

- the order of test elements of atc_1 and atc_2 is preserved;

- a test element of atc_1 (atc_2) triggered by an internal signal is constrained by the corresponding test element in atc_2 (atc_1) that produces this internal signal;
- the order of outputs is preserved.

Example 2: For the csFSMs N_1 and N_2 of Example 1, the following augmented test cases can be derived and composed:

- $atc_1 = x_1/i_1.i_2/x_4$
- $atc_2 = i_1/x_2.x_3/i_2$
- $atc_1 \parallel atc_2 = x_1/i_1.i_1/x_2.x_3/i_2.i_2/x_4$

In this case, the composition produces only one path because the test elements are totally ordered.

Definition 7: The concurrent composition of two augmented test cases is called *complete*, iff all their test elements are included, and the input queues of the corresponding csFSMs will be empty after their execution. Otherwise, it is called *incomplete*.

Example 3: The concurrent composition of atc_1 and atc_2 in Example 2 is complete. However, the concurrent composition of atc_1 and $atc_2' = i_1/x_2$ results in $x_1/i_1.i_1/x_2$, which is incomplete.

3.3 Initial tour coverage tree

Selected augmented test cases of components form the basis for deriving a test suite for validating the correct implementation of their composition. These test cases are derived from a so-called *initial tour coverage tree*, reduced to the set of relevant test cases, and composed with matching test cases of the other component.

Definition 8: Let $N = (S, I, O_e, s_0, \lambda)$ be a csFSM with the underlying graph G , where G is strongly connected. An *initial tour coverage tree* T is a tree containing all minimal initial tours such that every edge is covered at least once and no tour is contained as a prefix or a suffix of another tour in the set.

The rationale behind this choice is that (i) transition coverage can be achieved this way¹, and that (ii) both automata should be synchronized at least in their initial states, a criterion for composability. The concept of initial tour coverage is different from minimal transition tour, which visits every transition once and only once, but which also relies on stronger conditions to exist. To construct an initial tour coverage tree, we use a tree that, for a given state, captures all cycle free paths to the initial state, called *homing tree*:

Definition 9: Given a csFSM $N = (S, I, O_e, s_0, \lambda)$ and a state $s \in S$, where the underlying graph is strongly connected, a *homing tree* $H(s)$ is a minimal tree that covers all cycle-free paths of N leading from s to the initial state s_0 .

We give algorithms for the construction of homing trees and initial tour coverage trees in Tables 1 and 2, respectively. Both algorithms are illustrated.

1. Initial tour coverage is a reduced form of path coverage.

- Step 1: Start the construction of $H(s)$ with its root node n_r , labeled with s .
- Step 2: Assume that $H(s)$ has been constructed up to level k , $k \geq 1$. Then level $k+1$ is built by examining the nodes of level k :
- Step 2.1: A node n of level k is terminated, if its label is identical to the label of a node on level j , where $1 \leq j < k$, or if it is identical to s_0 .
- Step 2.2: Otherwise, let s denote the label of node n . Then, for all transitions (s, x, y, s') , attach a branch and successor node to the current node, labeled x/y and s' , respectively.
- Step 3: Prune the resulting tree by successively removing all leaf nodes that have a label $s \neq s_0$, and the corresponding edges.

Table 1: Construction of a homing tree $H(s)$

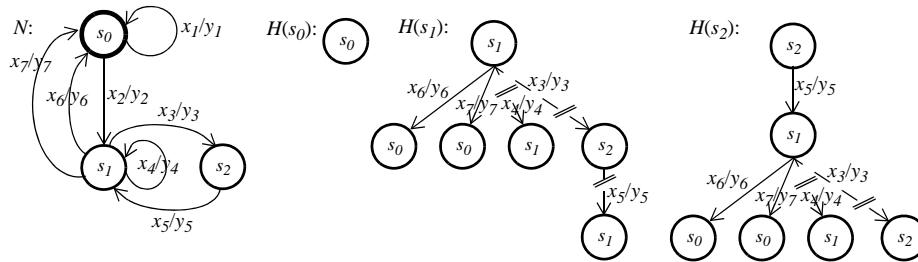


Figure 4 Homing trees (example)

- Step 1: For each state s of N , construct a homing tree $H(s)$.
- Step 2: Start the construction of T with the root node n_r , labeled with the initial state s_0 of N . This is level 1 of T .
- Step 3: Assume that T has been constructed up to level k , $k \geq 1$. Then level $k+1$ is built by examining the nodes of level k :
- Step 3.1: A node n of level k is terminated, if its label is identical to the label of a node on level j , where $1 \leq j < k$.
- Step 3.2: Otherwise, let s denote the label of n . Then, for each transition (s, x, y, s') , attach a branch and successor node to the current node, labeled x/y and s' , respectively.
- Step 4: To each leaf node n , attach the homing tree $H(s)$ by merging the root node of $H(s)$ with n , where s denotes the label of n .

Table 2: Construction of an initial tour coverage tree T

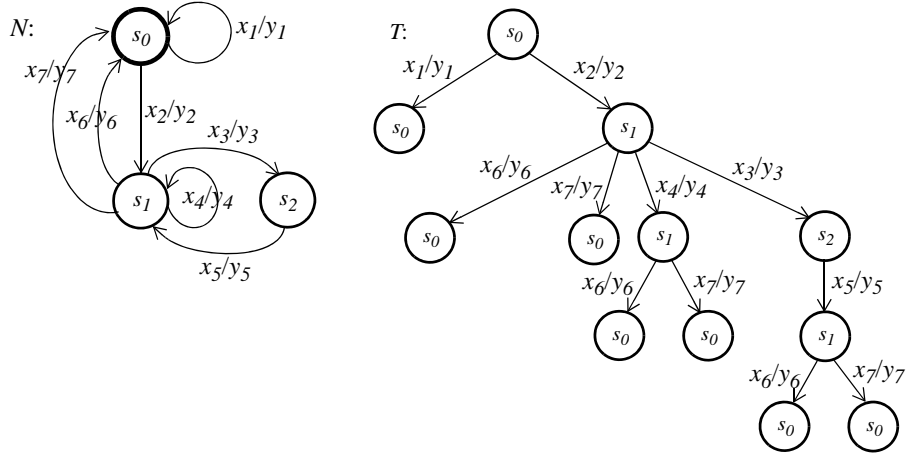


Figure 5 Initial tour coverage tree (example)

3.4 The C-method

In Section 2, we have stated general composition criteria CC_1 and CC_2 that should be satisfied for a meaningful composition at the design level. First, the composed system should be free of internal unspecified receptions, which means that receptions occurring during „normal operation“ have to be consumed by explicit transitions. This excludes transitions that have been added for mere technical reasons to obtain fully specified state machines (see Definition 2). Also, the composed system should be free of internal deadlocks.

To check whether two csFSMs N_1 and N_2 meet these criteria, we assume that they are always capable to resynchronize in their initial states. In other words, if N_1 is in its initial state and stays there, N_2 should be able to reach its initial state without further interaction with N_1 , and vice versa. If this assumption is satisfied, it suffices to consider the explicit initial tours of both automata, i.e., the explicit transition sequences starting and ending in the initial states, and to check whether for each explicit initial tour, there is a matching explicit initial tour of the other automaton such that their concurrent composition is complete. This design criterion can also be stated in terms of concurrent composition of augmented test suites, and thus be checked as a by-product of test case derivation.

In Table 3, the C-method is defined in a procedural style. We point out that in the course of applying the test procedure, it is checked whether N_1 and N_2 satisfy the composition criteria. This is a constraint imposed on design level, which should be checked before implementing the design and testing the implementation. Thus, all steps except Steps 1.2, 1.3, 2.8, and 2.9 should be executed in the design phase. Step 2.6 could be optimized further by reducing the number of considered compositions (see [5]).

As expected, the augmented test suites ats_1 and ats_2 are reduced to empty test suites in case N_1 and N_2 do not interact, i.e., in case of independent concurrent composition, which, among other things, satisfies the criterion for concurrent composability. The rea-

C-method

Step 1: Test the implementations I_1 and I_2 of components N_1 and N_2 .

Step 1.1: Select a test method (e.g., DS [11], UIOv [20], Wp [12]).

Step 1.2: Derive the test suites for N_1 and N_2 .

Step 1.3: Execute the tests. If all tests are successful, continue with Step 2.
If not, correct the faults and repeat Step 1.

Step 2: Test the implementation of the concurrent composition of N_1 and N_2 .

Step 2.1: Remove all transitions of N_1 and N_2 that yield an error output. These transitions have already been tested during component testing, and need not be tested again.

Step 2.2: Build the initial tour coverage trees for N_1 and N_2 , and determine all maximal paths, i.e., all paths that start at the root node and end at a leaf node, constituting augmented test suites ats_1 and ats_2 .

Step 2.3: From the augmented test suites ats_1 (ats_2), remove all internally triggered test cases, i.e., those test cases that are triggered by N_2 (N_1).

Step 2.4: From the augmented test cases, remove all local tours, i.e., (sub)sequences of test case elements that (1) start and end in the same state, and (2) contain only external inputs and outputs. They have already been checked during component testing, and need not be tested again.

Step 2.5: Remove the maximum suffix that does not contain an interaction with the other component. These test elements have been checked already.

Step 2.6: For each test case $atc_{1,j}$ of the augmented test suite ats_1 after Step 2.5, find an augmented test case $atc_{2,j}$ of N_2 from Step 2.2 such that $atc_{1,j} \parallel atc_{2,j}$ is complete, and determine $atc_{1,2,j} = atc_{1,j} \parallel atc_{2,j}$, yielding the concurrent augmented test suite $ats_{1,2}$. Analogously for each test case $atc_{2,j}$ of ats_2 .

Step 2.7: Based on ats_1 , ats_2 , and $ats_{1,2}$, check whether N_1 and N_2 meet the composition criteria CC_1 and CC_2 , i.e., whether for each test case of ats_1 (ats_2), there is a matching test case of N_2 (N_1). Yes: continue with Step 2.8; no: stop.

Step 2.8: For each test case in $ats_{1,2}$: merge adjacent test case elements in cases where (1) the internal output of the first matches the internal input of the second, and (2) the output is the only signal in the queue after being sent. Replace internal inputs and outputs by “-”, and remove test case elements “-/-”.

Step 2.9: Execute the test.

Table 3: The C-method

son is that all necessary testing has already been done on component level. Of course, one can argue that in the implementation, interaction of the two components may occur, and has to be excluded. This, however, is not covered by this type of tests. When protocol components are reused, it is sufficient to test them once, which means in a certain sense that testing is reused, too. In these cases, compositional testing starts with Step 2.

4 Application of the C-method

To illustrate the C-method, we apply it to the Initiator Responder (InRes) protocol [9]. The InRes protocol is a connection-oriented communication protocol for the reliable exchange of message over an order-preserving, connection-less medium. It provides an asymmetrical service: the initiator requests connections and sends data, the responder accepts, refuses, and clears connections, and receives data. In this example, the InRes protocol entities I and R are the components that are composed concurrently, yielding a composite system $I \parallel R$. In the implementation of this system, the glue code is represented by the underlying medium. To be able to use this medium for the implementation of the $I \parallel R$, we assume that it does not lose messages.

Figure 6 shows the specifications I and R of the InRes protocol entities and their concurrent composition. Both automata contain further transitions that can be derived by applying Definition 2, and thus are fully-specified. To avoid cluttering, we have omitted these transitions in the figure. The underlying graphs are deterministic, and strongly connected. We assume that Step 1 of the C-method that tests the implementations of I and R separately has already been executed successfully. Below, we go through Step 2:

- Step 2.1: Removal of transitions yielding an error output

These transitions have been omitted in the figure, therefore, starting point for Step 2.2 are the finite state automata shown in Figure 6.

- Step 2.2: Build initial tour coverage trees, and determine ats_I and ats_R

The initial tour coverage trees for I and R are shown in Figure 7. Test suites are:

$$ats_I = \{atc_{I,1}, atc_{I,2}, atc_{I,3}, atc_{I,4}\}, \text{ with}$$

$$atc_{I,1} = \text{ICONreq/CR} \cdot \text{DR/IDISind}$$

$$atc_{I,2} = \text{ICONreq/CR} \cdot \text{CC/ICONcnf} \cdot \text{DR/IDISind}$$

$$atc_{I,3} = \text{ICONreq/CR} \cdot \text{CC/ICONcnf} \cdot \text{IDATreq/DT} \cdot \text{DR/IDISind}$$

$$atc_{I,4} = \text{ICONreq/CR} \cdot \text{CC/ICONcnf} \cdot \text{IDATreq/DT} \cdot \text{AK/-} \cdot \text{DR/IDISind}$$

$$ats_R = \{atc_{R,1}, atc_{R,2}, atc_{R,3}, atc_{R,4}\}, \text{ with}$$

$$atc_{R,a} = \text{DT/-}$$

$$atc_{R,b} = \text{CR/ICONind} \cdot \text{IDISreq/DR}$$

$$atc_{R,c} = \text{CR/ICONind} \cdot \text{ICONrsp/CC} \cdot \text{IDISreq/DR}$$

$$atc_{R,d} = \text{CR/ICONind} \cdot \text{ICONrsp/CC} \cdot \text{DT/IDATind} \cdot \text{-/AK} \cdot \text{IDISreq/DR}$$

- Step 2.3: Remove test cases triggered by internal inputs

All test cases of R are triggered by inputs of the Initiator and therefore removed:

$$ats_I' = \{atc_{I,1}, atc_{I,2}, atc_{I,3}, atc_{I,4}\}$$

$$ats_R' = \{\}$$

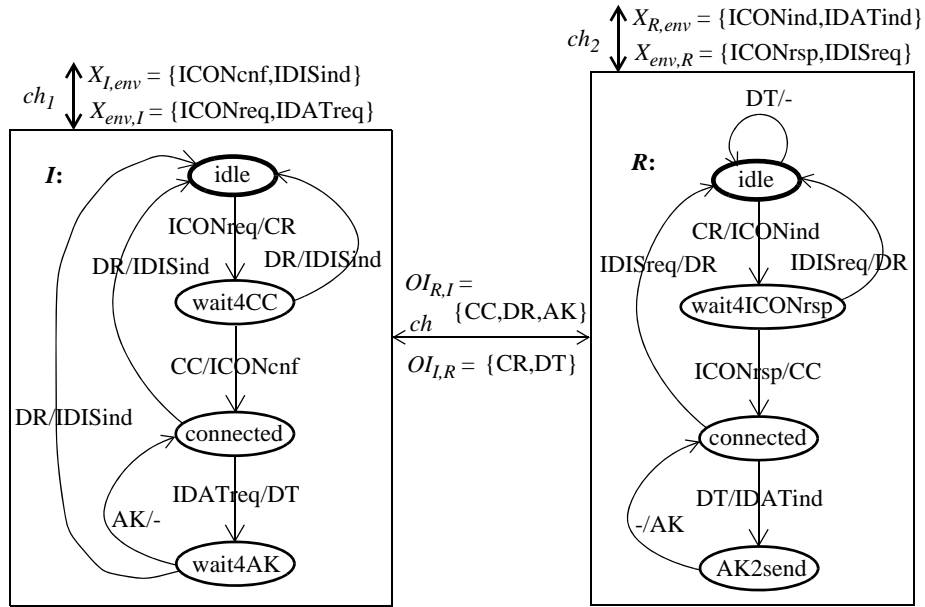


Figure 6 InRes protocol entities *I* and *R*

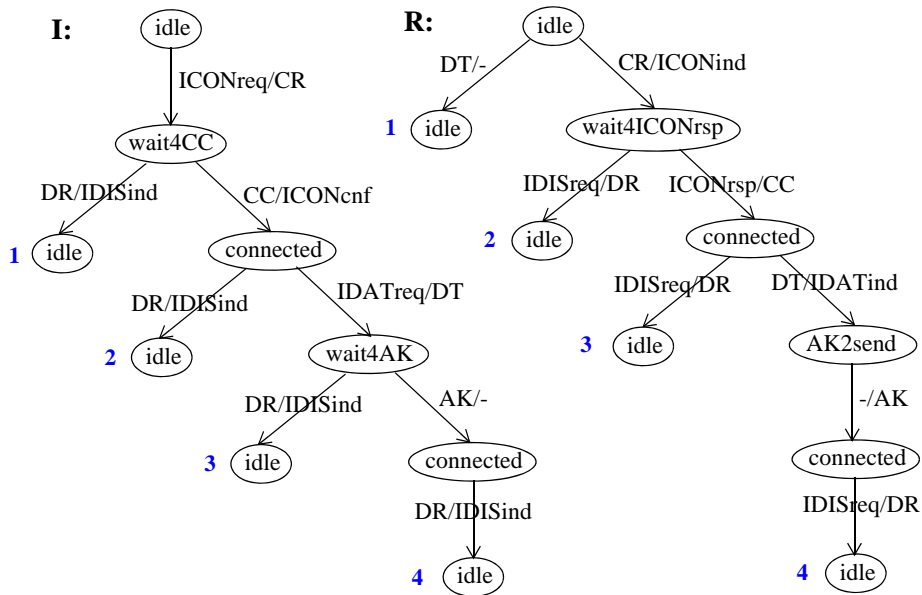


Figure 7 Initial tour coverage trees of *I* and *R*

- Step 2.4: Remove external local tours
Not applicable in the InRes example.
- Step 2.5: Remove suffix containing external interaction only.
Not applicable in the InRes example.
- Step 2.6: For each augmented test case in ats_I (ats_R) after Step 2.5, find an augmented test case of R (I) from Step 2.2 such that their concurrent composition is complete, and determine the concurrent augmented test suite $ats_{I,2}$.

$ats_{I,2} = \{ atc_{I,1} \parallel atc_{R,a}, atc_{I,2} \parallel atc_{R,b}, atc_{I,3} \parallel atc_{R,c}, atc_{I,4} \parallel atc_{R,d} \}$, with:

$atc_{I,1} = \text{ICONreq/CR} \cdot \text{DR/IDISind}$

$atc_{R,a} = \text{CR/ICONind} \cdot \text{IDISreq/DR}$

$atc_{I,1} \parallel atc_{R,a} = \{ \text{ICONreq/CR} \cdot \text{CR/ICONind} \cdot \text{IDISreq/DR} \cdot \text{DR/IDISind} \}$

$atc_{I,2} = \text{ICONreq/CR} \cdot \text{CC/ICONcnf} \cdot \text{DR/IDISind}$

$atc_{R,b} = \text{CR/ICONind} \cdot \text{ICONrsp/CC} \cdot \text{IDISreq/DR}$

$atc_{I,2} \parallel atc_{R,b} = \{ \text{ICONreq/CR} \cdot \text{CR/ICONind} \cdot \text{ICONrsp/CC} \cdot \text{CC/ICONcnf} \cdot \text{IDISreq/DR} \cdot \text{DR/IDISind}, \text{ICONreq/CR} \cdot \text{CR/ICONind} \cdot \text{ICONrsp/CC} \cdot \text{IDISreq/DR} \cdot \text{CC/ICONcnf} \cdot \text{DR/IDISind} \}$

$atc_{I,3} = \text{ICONreq/CR} \cdot \text{CC/ICONcnf} \cdot \text{IDATreq/DT} \cdot \text{DR/IDISind}$

$atc_{R,c} = \text{CR/ICONind} \cdot \text{ICONrsp/CC} \cdot \text{IDISreq/DR} \cdot \text{DT/-}$

$atc_{I,3} \parallel atc_{R,c} = \{ \text{ICONreq/CR} \cdot \text{CR/ICONind} \cdot \text{ICONrsp/CC} \cdot \text{CC/ICONcnf} \cdot \text{IDATreq/DT} \cdot \text{IDISreq/DR} \cdot \text{DR/IDISind} \cdot \text{DT/-}, \text{ICONreq/CR} \cdot \text{CR/ICONind} \cdot \text{ICONrsp/CC} \cdot \text{CC/ICONcnf} \cdot \text{IDATreq/DT} \cdot \text{IDISreq/DR} \cdot \text{DT/-} \cdot \text{DR/IDISind}, \text{ICONreq/CR} \cdot \text{CR/ICONind} \cdot \text{ICONrsp/CC} \cdot \text{CC/ICONcnf} \cdot \text{IDISreq/DR} \cdot \text{IDATreq/DT} \cdot \text{DT/-} \cdot \text{DR/IDISind}, \text{ICONreq/CR} \cdot \text{CR/ICONind} \cdot \text{ICONrsp/CC} \cdot \text{IDISreq/DR} \cdot \text{CC/ICONcnf} \cdot \text{IDATreq/DT} \cdot \text{DT/-} \cdot \text{DR/IDISind} \}$

$atc_{I,4} = \text{ICONreq/CR} \cdot \text{CC/ICONcnf} \cdot \text{IDATreq/DT} \cdot \text{AK/-} \cdot \text{DR/IDISind}$

$atc_{R,d} = \text{CR/ICONind} \cdot \text{ICONrsp/CC} \cdot \text{DT/IDATind} \cdot \text{-/AK} \cdot \text{IDISreq/DR}$

$atc_{I,4} \parallel atc_{R,d} = \{ \text{ICONreq/CR} \cdot \text{CR/ICONind} \cdot \text{ICONrsp/CC} \cdot \text{CC/ICONcnf} \cdot \text{IDATreq/DT} \cdot \text{DT/IDATind} \cdot \text{-/AK} \cdot \text{AK/-} \cdot \text{IDISreq/DR} \cdot \text{DR/IDISind}, \text{ICONreq/CR} \cdot \text{CR/ICONind} \cdot \text{ICONrsp/CC} \cdot \text{CC/ICONcnf} \cdot \text{IDATreq/DT} \cdot \text{DT/IDATind} \cdot \text{-/AK} \cdot \text{IDISreq/DR} \cdot \text{AK/-} \cdot \text{DR/IDISind} \}$

- Step 2.7: Check the composition criteria CC_1 and CC_2
For each test case of ats_I , there is a test case of R such that their concurrent composition is complete. This trivially holds for ats_R , which is empty.
- Step 2.8: Merge test case elements, and replace internal inputs and outputs by „,“

$ats_{I,2} = \{ atc_{I,1} \parallel atc_{R,a}, atc_{I,2} \parallel atc_{R,b}, atc_{I,3} \parallel atc_{R,c}, atc_{I,4} \parallel atc_{R,d} \}$, with:

$atc_{I,1} \parallel atc_{R,a} = \{ \text{ICONreq/ICONind} \cdot \text{IDISreq/IDISind} \}$

$atc_{I,2} \parallel atc_{R,b} = \{ \text{ICONreq/ICONind} \cdot \text{ICONrsp/ICONcnf} \cdot \text{IDISreq/IDISind} \}$

$atc_{I,3} \parallel atc_{R,c} = \{ \text{ICONreq/ICONind} \cdot \text{ICONrsp/ICONcnf} \cdot \text{IDATreq/-} \parallel \text{IDISreq/-} \cdot \text{-/IDISind} \}$

$$atc_{I,4} \parallel atc_{R,d} = \{ \text{ICONreq/ICONind} \cdot \text{ICONrsp/ICONcnf} \cdot \text{IDATreq/IDATind} \cdot \text{IDISreq/IDISind} \}$$

Note that test case $atc_{I,3} \parallel atc_{R,c}$ requires that test input IDATreq and IDISreq are to be applied concurrently to stimulate this behavior. This is expressed by the notation [$tce_1 \parallel tce_2$]. The resulting test suite $ats_{I,2}$ consists of 4 test cases, with 14 test case elements. In addition, component tests are to be performed.

5 Related work

The purpose of this section is not to review deeply all the rich literature on FSM-based testing, but to position the proposed C-method with respect to the related types of testing such as interoperability testing, testing in context and compositional testing.

5.1 Interoperability testing

Interoperability testing [1,2,4,5] aims at checking if two implementations, which are conforming to a common specification, interact correctly and provide a required service when interconnected through a communication medium. The communication medium, i.e. the glue between the two protocol entities, is assumed to behave correctly. This is different from the C-Method, where we assume that any integration problem or fault is coming from the glue, once the components have been individually tested. In addition, the C-Method also checks whether a required service is provided (see Step 2.7 - composition criteria checking). However, these general composition criteria are checked at the specification level in case of the C-Method, while it is done at testing time in case of interoperability testing.

5.2 Testing in context

Testing in context [14,15] consists of testing a component C_p in a given context C_x formed by other components, with the purpose of detecting faults in C_p . The component C_p is generally not directly observable from the environment or only partially. The specifications of context C_x and component C_p are both available.

Testing in context is about testing the component C_p , not the behavior of the whole system. However, since the component is not directly accessible, or only partially, but it is tested through its context. Therefore, we select the observable behavior of the global system that will stimulate the behavior of the component as much as possible, and interpret the system output. This system reaction is generally coming from the context following a reaction from the component under test. The global state space is generally not constructed.

The aim of the C-method is to test a composed system that consists of n components by testing individually each component against its specification, by checking the composability of these components, and by testing the glue, which is putting all these components together to obtain a particular system. It aims at validating the whole system instead of the glue in context only, but by testing only portions of the system behavior. Once the components are tested successfully, their behavior is not questioned anymore. If an error happens, only the behavior of the glue is in question.

5.3 Compositional testing

An approach for compositional testing has been proposed in [19]. It is based on *ioco* and therefore on a synchronous communications setting. The aim of this approach is to find the conditions under which the conformance of the components to their respective specification leads automatically, without any extra testing, to the conformance of the system implementation to the system specification. The operator considered so far is parallel composition. There is no glue code in this approach.

6 Conclusions and future work

In this paper, the *compositional method (C-method)* for testing communicating systems has been introduced. The C-method first tests each protocol component separately for component faults (output and/or transfer faults), using one of the traditional test methods, and then checks their composition for composition faults.

To apply the C-method, it is not necessary to compute the global state machine. Instead, composition tests are derived from local initial tour coverage trees. Only tests checking the glue code are derived. We have introduced and justified a fault model for the glue code that leads to manageable composition test suites.

The work on compositional testing has been triggered by the component based software engineering trend and our results on micro protocols [8], a concept to structure communication systems and to foster reuse of protocol designs. Micro protocols are protocols with a single (distributed) functionality and the required collaboration among protocol entities. To develop customized communication systems, micro protocol designs are selected from a library, composed to yield a complete design, and implemented. We expect that the C-method will contribute to the testing of customized communication systems that are composed of micro protocols.

The results presented in this paper leave room for further work. The following improvements and enhancements are perceivable:

- So far, only composition of two FSMs has been considered. It would be useful to extend the C-method to compositions of more than two FSMs, and also to the composition of composites that have already been tested successfully.
- Other types of compositions, for instance, concurrent composition with shared variables, or composition through inheritance, are perceivable. Again, this requires extensions to the C-method.
- The justification of the C-method and its benefits should be treated more rigorously, developing a test theory rich enough to provide a formal proof that the derived test suite is both necessary and sufficient to detect composition faults.
- The complexity of the C-method in comparison to other testing approaches should be formally assessed. Since the C-method exploits the structure of the system under test to reduce the number and length of test cases, we expect significant improvements.

Finally, a generic testing approach, where interoperability testing, testing in context, and compositional testing are seen as specific instances with different goals and assumptions, will be an interesting research issue to pursue.

7 References

- [1] R. Castanet, O. Kone: *Deriving Coordinated Testers for Interoperability*, Protocol Test Systems, Volume VI C-19, Pau, France, 1994
- [2] R. Castanet and O. Kone: *Test Generation for Interworking Systems*, Computer Communications, Elsevier, Vol. 23, 2000, pp. 642-652.
- [3] T. S. Chow: *Testing Software Design Modeled by Finite-State Machines*, IEEE Transactions on Software Engineering, Vol. 4, No. 3, 1978, pp. 178-187
- [4] A. Desmoulin, C. Viho: *Quiescence Management Improves Interoperability Testing*, Proceedings of TestCom 2005, LNCS 3502, Springer, pp. 365-379
- [5] A. Desmoulin, C. Viho: *Formalizing interoperability testing: Quiescence management and test generation*, Proceedings of FORTE'2005, LNCS 3731, Springer, pp. 533-537
- [6] A. En-Nouaary, R. Dssouli, F. Khendek: *Timed Wp: Testing Real-Time Systems*, IEEE Transactions on Software Engineering, Vol. 28, No. 11, November 2002, pp. 1023-1038
- [7] S. Fujiwara, G. v. Bochmann, F. Khendek, M. Amalou, A. Ghedamsi: *Test Selection Based on Finite State Models*, IEEE ToSE, Vol. 17, No. 6, June 1991, pp. 591-603
- [8] R. Gotzhein, F. Khendek, P. Schaible: *Micro Protocol Design: The SNMP Case Study*, SDL and MSC Workshop (SAM'2002), LNCS 2599, Springer, 2002
- [9] D. Hogrefe: *OSI Formal Specification Case Study: The InRes Protocol and Service, revised*, Report No. IAM-91-012, Update May 1992, University of Berne, May 1992
- [10] ITU-T Recommendation Z.100 (11/99) - *Specification and Description Language (SDL)*, International Telecommunication Union (ITU), 1999
- [11] Z. Kohavi: *Switching and Finite Automata Theory*, McGraw Hill, USA, 1978
- [12] G. Luo, G. v. Bochmann, A. Petrenko: *Test Selection Based on Communicating Nondeterministic Finite-State Machines Using a Generalized Wp-Method*, IEEE Transactions on Software Engineering, Vol. 20, No. 2, 1994, pp. 149-162
- [13] A. Petrenko, G. v. Bochmann, M. Yao: *On Fault Coverage of Tests for Finite State Specifications*, Computer Networks and ISDN Systems, Vol. 29, 1996, pp. 81-106
- [14] A. Petrenko, N. Yevtushenko, G. v. Bochmann, R. Dssouli: *Testing in Context: Framework and Test Derivation*, Computer Communications, Elsevier, Vol. 19, 1996, pp. 1236-1249
- [15] A. Petrenko, N. Yevtushenko, G. v. Bochmann: *Fault Models for Testing in Context*, Proceedings of FORTE'96, pp. 163-178
- [16] B. Sarikaya, G. v. Bochmann: *Some Experience with Test Sequence Generation for Protocols*, Proceedings of the 2nd International Workshop on Protocol Specification, Testing, and Verification, North Holland, 1982, pp. 555-567
- [17] J. Springintveld, F. W. Vaandrager, P. R. D'Argenio: *Testing timed automata*, Theoretical Computer Science, Vol. 254 (1-2), 2001, pp. 225-257
- [18] M. Ü. Uyar, Y. Wang, S. S. Batth, A. Wise, M. A. Fecko: *Timing Fault Models for Systems with Multiple Timers*, Proceedings of TestCom 2005, LNCS 3502, Springer, 192-208
- [19] M. van der Bijl, A. Rensink, J. Tretmans: *Compositional Testing with ioco*, Proceedings of FATES 2003, LNCS 2931, 2003
- [20] S. T. Vuong, W. W. L. Chan, M. R. Ito: *The UIOv-Method for Protocol Test Sequence Generation*, 2nd International Workshop on Protocol Test Systems, Berlin, Germany, 1989
- [21] E. J. Weyuker: *Testing Component Based Software: A Cautionary Tale*, IEEE Software, September/October 1998, pp.54-59